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Initials*	No.'			Applicant of Cited Document		Relevant Passages or Relevant Figures Appear	
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Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> 'Kind Code <sup>9</sup> ( <i>If known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>©</sup>			
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Substitute for form 1449B/PTO					Complete if Known			
					Application Number	N/A		
		RMATION DI			Filing Date	Herewith		
STATEMENT BY APPLICANT					First Named Inventor	Paul Gerard D'Arcy		
					Art Unit	N/A		
		(Use as many sheets as	necessary	<i>(</i> )	Examiner Name	N/A		
7	Sheet	2	of	2	Attorney Docket Number	D'ARCY 15-6-7		

	NON PATENT LITERATURE DOCUMENTS	
Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
1	C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.	
	K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.	
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	No.¹	Cite No.1  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, calalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.  C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.  K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.  I. Koren; Computer Arithmetic Algorithms; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.  J. Postel; Internet Protocol; RFC 791; University of Southern California, Information Sciences Institute; September 1981; pp. 1-45.  J. Postel; Transmission Control Protocol; RFC 793; University of Southern California, Information Sciences Institute; September 1981; pp. 1-85.  J. Postel; IUser Datagram Protocol; RFC 768; August 1980; pp. 1-3.  L. Dadda; Some Schemes For Parallel Multipliers; Alta Frequenza; Vol. 34; pp. 349-356; 1965.  C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on

Examiner Signature	R. Steph	en Deldine	Date Considered	5 January 2006
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